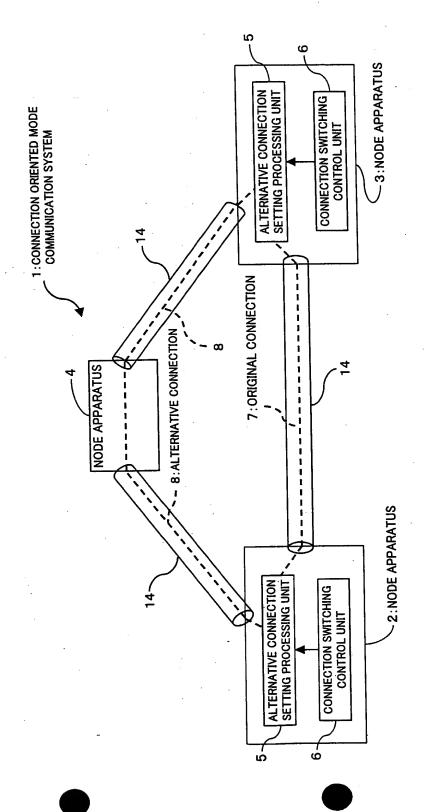
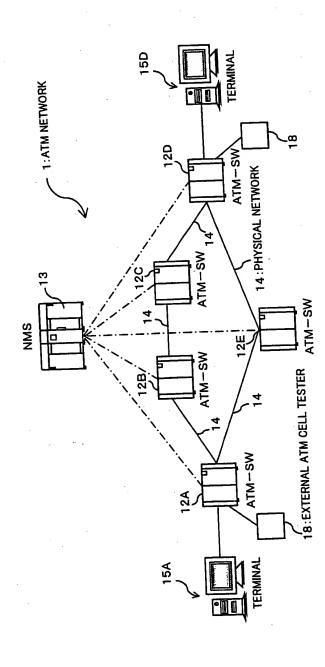
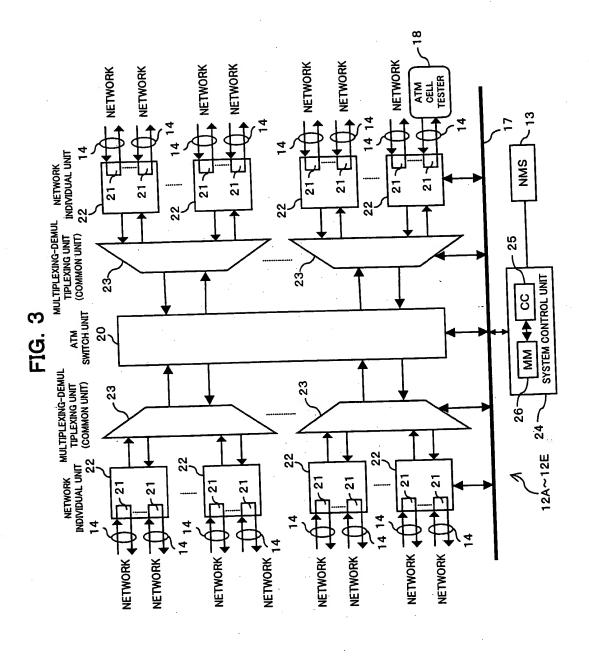
FIG. 1







23:COMMON OAM PROCESSING UNIT CHARGING/NDC PROCESSING UNIT 22(22A) PROCESSOR UPC/NPC PROCESSING UNIT HEADER INVERSE CONVERSION UNIT (VPI/VCI — I-ICID) HEADER CONVERTING UNIT (VPI/VCI → I-ICID) MĖMORY CELL SELECTING UNIT CELL COPY UNIT PHYSICAL TERMINATING UNIT 21 OUTPUT NETWORK IF UNIT INPUT NETWORK
IF UNIT PORT n▲ PORT 0 PORT n . PORT 0-14:PHYSICAL 14:PHYSICAL NETWORK

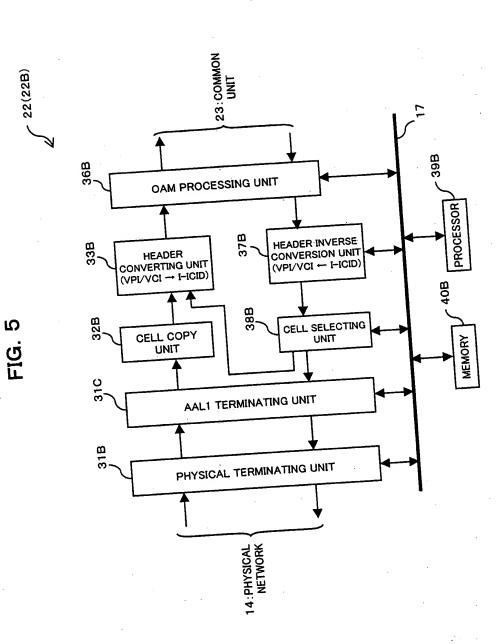


FIG. 6

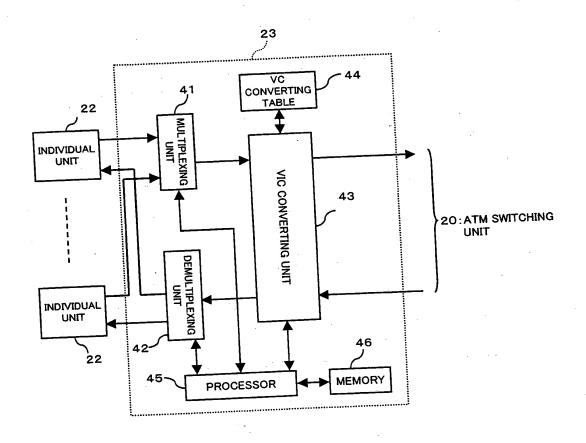


FIG. 7(a)



		0000	
TARGET	COPY SOURCE	COPY DESTINATION	Alternative
VPI∕VCI	VPI/VCI	VPI∕VCI	Route
	170 (100	51/100	0
0/32	50/100	517 100	
		:	:
		<u> </u>	· • • • • • • • • • • • • • • • • • • •
∧	/	7	(
ſ	- 1	_'_	5 ¹ d
51a	51b	51c	J.4

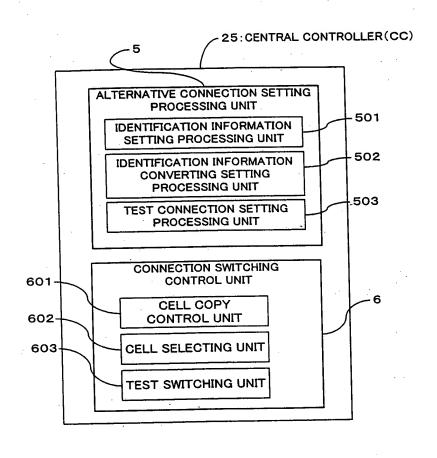
FIG. 7(b)

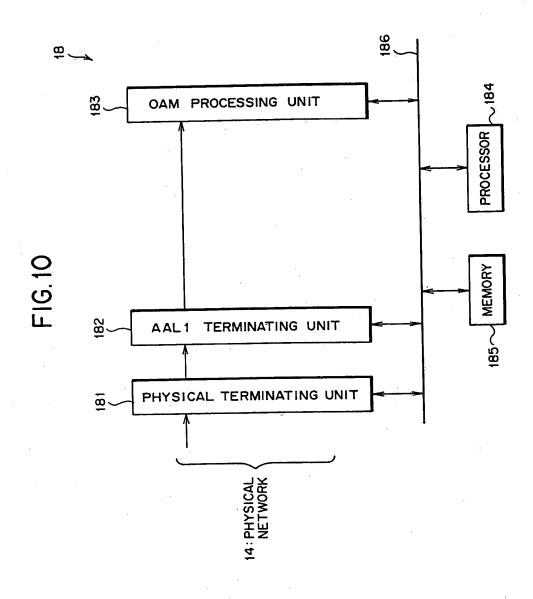
0	Alternative Route NOT UTILIZED
1	Alternative Route UTILIZED

RECEIVING SIDE HEADER INVERSE CONVERSION TABLE

I-ICID-A"	VPI/VCI	Enable
100	0/32	1
101	0/32	0
101	07 02	

FIG. 9





F1G 1

PARAMETER	CONTENTS	NOTE
۲۸	CODING VIOLATION	ABOUT BIT ERROR, PRESENT OR NOT
ES	ERRORED SECOND	NUMBER OF DETECTED EVENTS OF CV, AIS AND SO ON
		NO OS GIAS OS SIG OS
SES	SEVCRELY ERRORED SECOND	ERROR SUCH AS CV, AIS AND SO ON LARGER THAN ES
SAS	SEF/AiS SECOND	
AiSS	Ais SECOND	
UASP	UNAVAILABLE SECOND	

FIG.12

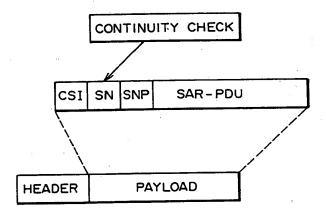


FIG.13

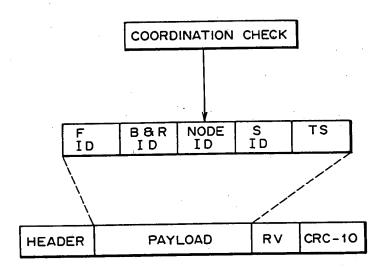
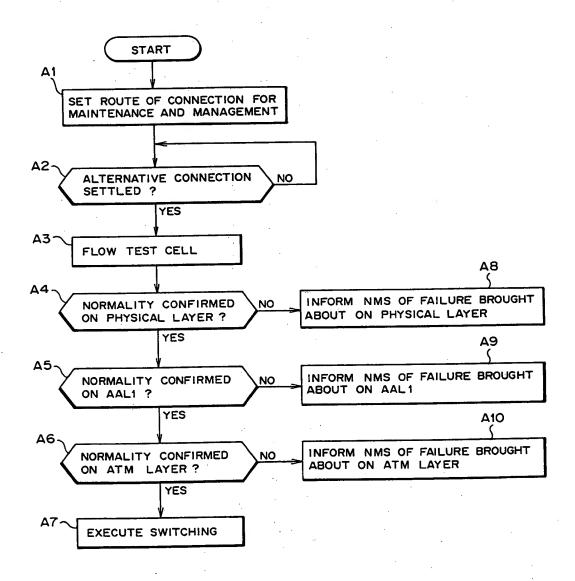
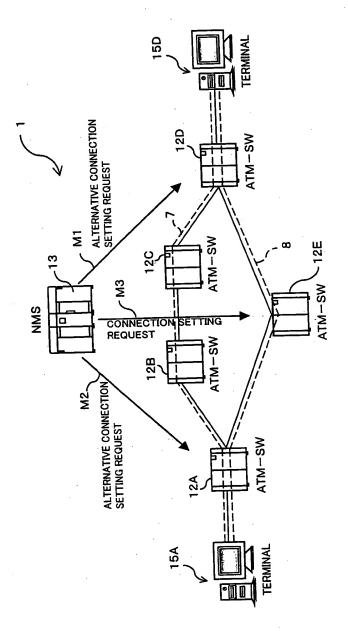
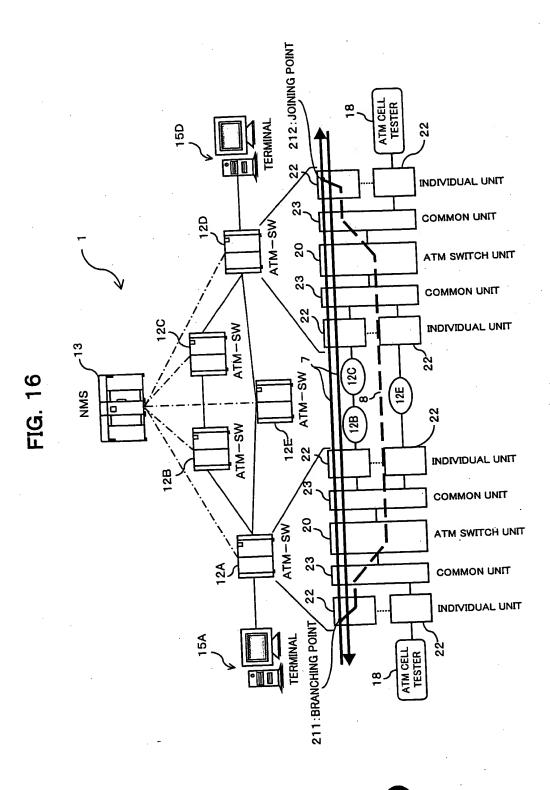


FIG.14



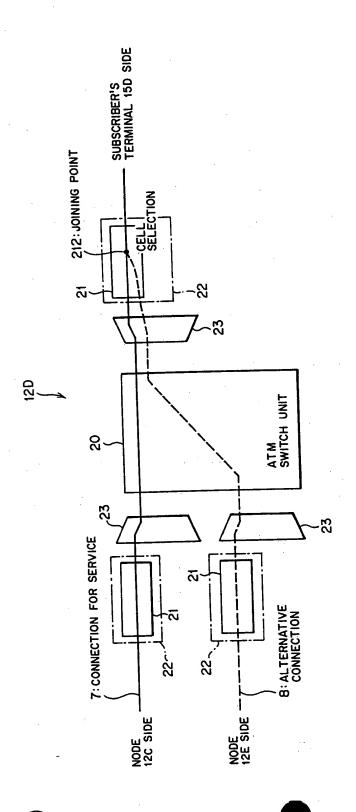
בור בו

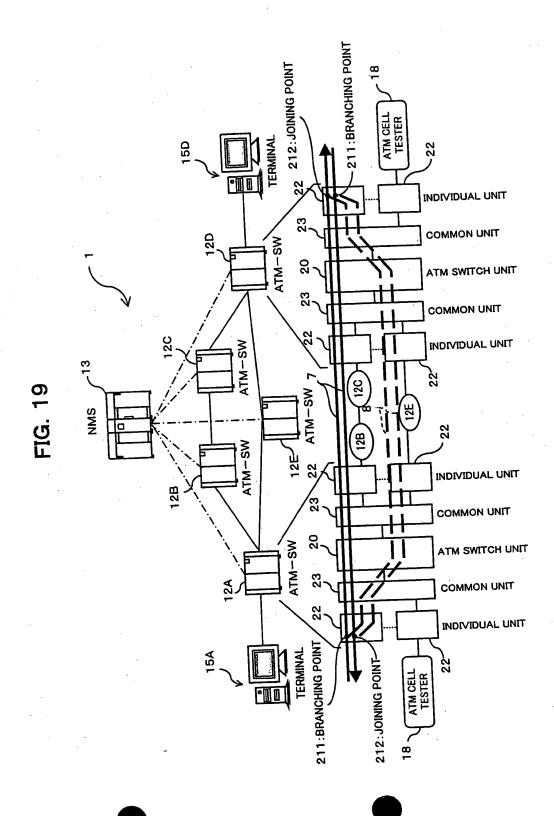


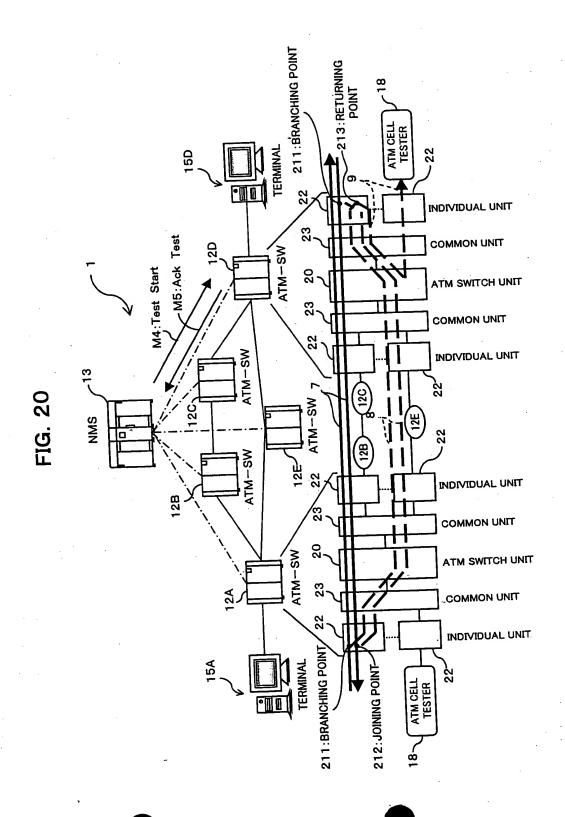


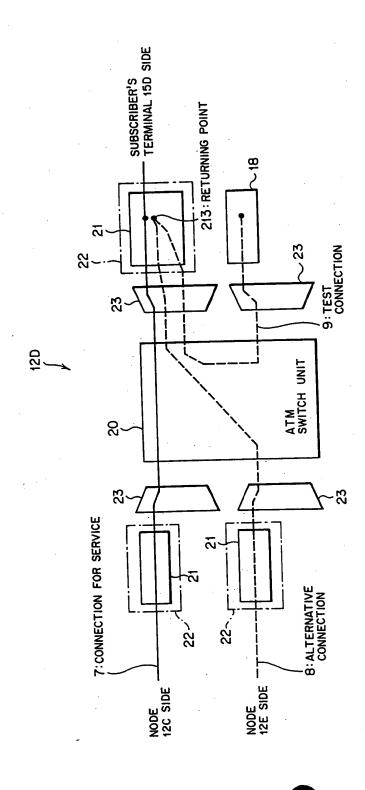
- ω ATM SWITCH UNIT 211: BRANCHING POINT (CELL COPY) SUBSCRIBER'S TERMINAL 15A SIDE

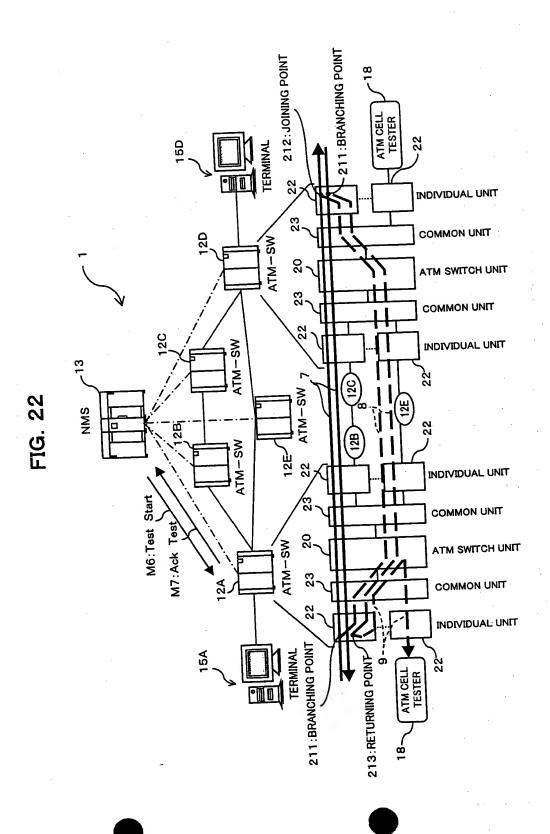
F1G.18











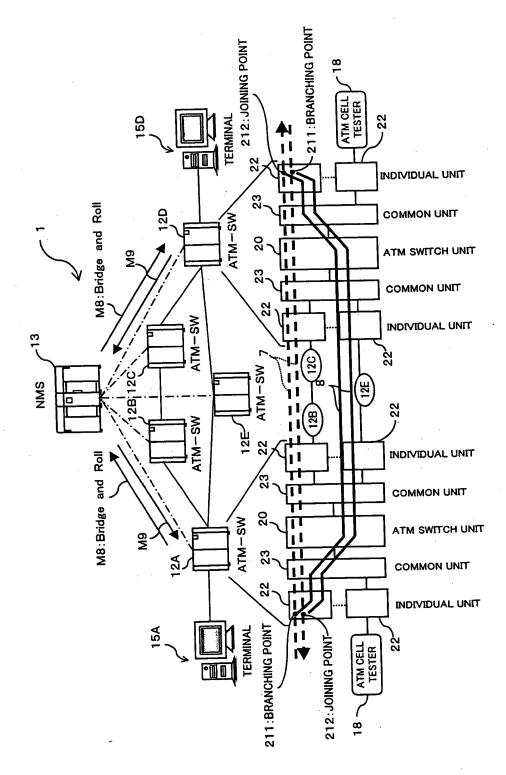
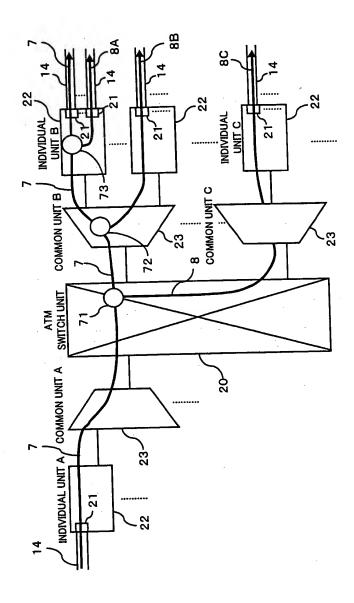


FIG. 23

FIG. 24



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FIG. 25

